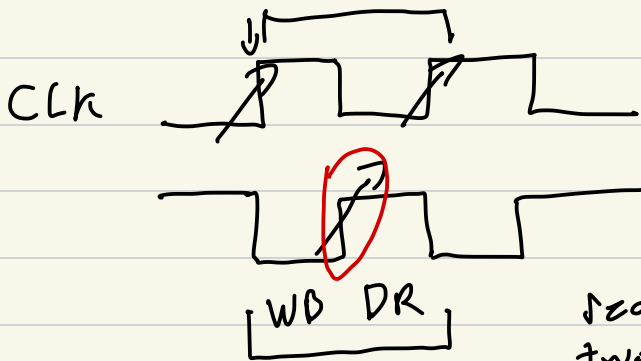


CS 315-01 Processor Design Hazard Unit

Pipelining

4 types of modifications

- 1) Inverting the clock to the RegFile so that WB can happen on first half of the clock cycle



Reduce to
two nops for
data

2) Forwarding

3) LD stalling

4) Control Hazard